

SKiM® 93

Hybrid SiC Trench IGBT Modules

SKiM459GD12F4V4

Features*

- IGBT 4 Fast
- SiC Schottky free-wheeling diodes, 6 diodes per switch
- · Solderless sinter technology
- V_{CE(sat)} with positive temperature coefficient
- Low inductance case
- Insulated by Al₂O₃ DBC (Direct Bonded Copper) ceramic substrate
- Pressure contact technology for thermal contacts
- Spring contact system to attach driver PCB to the control terminals
- High short circuit capability
- Integrated temperature sensor
- UL recognized: File no. E63532

Typical Applications

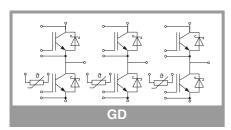
- UPS (inv., rect.)
- Energy storage
- · Active front-end

Remarks

 Case temperature limited to T_s = 125°C max; T_c = T_s (for baseplateless modules)

Footnotes

 $I_{\rm FSM}$ value is valid for SiC Schottky diode in combination with IGBT, please see Technical Explanations SKiM63/93 for further details



Absolute	Maximum Ratings	3		
Symbol	Conditions		Values	Unit
Inverter -	IGBT			
V _{CES}	T _j = 25 °C		1200	V
Ic	$\lambda_{paste}=0.8 \text{ W/(mK)}$	T _s = 25 °C	589	Α
T	T _j = 175 °C	T _s = 70 °C	476	Α
I _C	λ _{paste} =2.5 W/(mK)	T _s = 25 °C	687	Α
	T _j = 175 °C	T _s = 70 °C	558	Α
I _{Cnom}			450	Α
I _{CRM}			1350	Α
V_{GES}			-20 20	V
t _{psc}	$V_{CC} = 800 \text{ V}$ $V_{GE} \le 15 \text{ V}$ $V_{CES} \le 1200 \text{ V}$	T _j = 150 °C	10	μѕ
Tj			-40 175	°C
Inverse -	Diode			
V_{RRM}	T _i = 25 °C		1200	V
I _F	λ _{paste} =0.8 W/(mK)	T _s = 25 °C	401	Α
	T _j = 175 °C	T _s = 70 °C	323	Α
I _F	λ_{paste} =2.5 W/(mK) T _j = 175 °C	T _s = 25 °C	455	Α
		T _s = 70 °C	367	Α
I _{FRM}			500	Α
I _{FSM}	t _p = 10 ms, sin 180°, T _j = 150 °C		1048 ¹⁾	Α
Tj			-40 175	°C
Module				•
I _{t(RMS)}	T _{terminal} = 80 °C,		700	Α
T _{stg}			-40 125	°C
V _{isol}	AC sinus 50 Hz, t =	: 1 min	2500	V

Characteristics								
Symbol	Conditions		min.	typ.	max.	Unit		
Inverter - IGBT								
V _{CE(sat)}	$I_{\rm C} = 450 {\rm A}$	T _j = 25 °C		2.05	2.42	V		
	V _{GE} = 15 V chiplevel	T _j = 150 °C		2.59	2.96	V		
V _{CE0}	chiplevel	T _j = 25 °C		1.10	1.28	V		
		T _j = 150 °C		0.95	1.13	V		
	V _{GE} = 15 V	T _j = 25 °C		2.1	2.5	mΩ		
	chiplevel	T _j = 150 °C		3.6	4.1	mΩ		
$V_{GE(th)}$	$V_{GE} = V_{CE}, I_C = 15.6$	6 mA	5.2	5.8	6.4	V		
I _{CES}	$V_{GE} = 0 \text{ V}, V_{CE} = 12$	00 V, T _j = 25 °C			6	mA		
C _{ies}	V 05.V	f = 1 MHz		26.4		nF		
C _{oes}	V _{CE} = 25 V V _{GE} = 0 V	f = 1 MHz		1.74		nF		
C _{res}		f = 1 MHz		1.41		nF		
Q_{G}	V _{GE} = - 8 V+ 15 V			2550		nC		
R _{Gint}	T _j = 25 °C			1.7		Ω		
t _{d(on)}	$I_{\rm C} = 250 {\rm A}$	T _j = 150 °C		258		ns		
t _r		T _j = 150 °C		30		ns		
Eon		T _j = 150 °C		5		mJ		
t _{d(off)}		T _j = 150 °C		517		ns		
t _f				73		ns		
E _{off}	$L_s = 30 \text{ nH}$	T _j = 150 °C		20		mJ		
R _{th(j-s)}	per IGBT, λ _{paste} =0.8 W/(mK)			0.069		K/W		
$R_{th(j-s)}$	per IGBT, λ _{paste} =2.5 W/(mK)			0.053		K/W		



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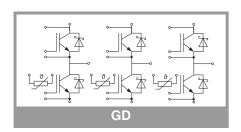
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- Energy storage
- · Active front-end

Remarks

• Case temperature limited to $T_s = 125^{\circ}C$ max; $T_c = T_s$ (for baseplateless modules)

Footnotes

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Characte	eristics					
Symbol	Conditions	min.	typ.	max.	Unit	
Inverse -	Diode					•
$V_F = V_{EC}$	I _F = 250 A	T _j = 25 °C		1.33	1.51	V
	chiplevel	T _j = 150 °C		1.63	1.90	V
V _{F0}	chiplevel	T _j = 25 °C		0.95	1.05	٧
		T _j = 150 °C		0.80	0.90	V
r _F	chiplevel	T _j = 25 °C		1.50	1.83	mΩ
		T _j = 150 °C		3.3	4.0	mΩ
C_j	$V_R = 800 \text{ V}, f = 1 \text{ MHz}, T_j = 25 ^{\circ}\text{C}$			1.260		nF
Q_c	$V_R = 800 \text{ V}, \text{ di/dt}_{off} = 500 \text{ A/}\mu\text{s},$ $T_i = 25 ^{\circ}\text{C}$			1		μС
R _{th(j-s)}	per Diode, λ _{paste} =0.8 W/(mK)			0.142		K/W
R _{th(j-s)}	per Diode, λ _{paste} =2.5 W/(mK)			0.115		K/W
Module						
L _{CE}				10	15	nΗ
R _{CC'+EE'}	measured per switch	T _s = 25 °C		0.3		mΩ
		T _s = 125 °C		0.5		mΩ
W				1042		g
Temperat	ture Sensor					_
R ₁₀₀	T _r =100°C (R ₂₅ =1000Ω)			1670 ± 1%		Ω
R _(T)	R(T)=1k Ω [1+A(T-25°C)+B(T-25°C) ²], A = 7.64*10 ⁻³ °C ⁻¹ , B = 1.73*10 ⁻⁵ °C ⁻²					

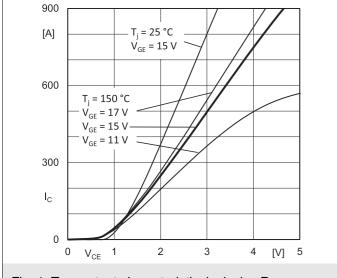


Fig. 1: Typ. output characteristic, inclusive $R_{\text{CC}'\text{+ EE'}}$

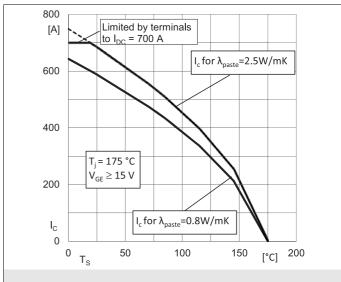


Fig. 2: Typ. rated current vs. temperature $I_C = f(T_S)$

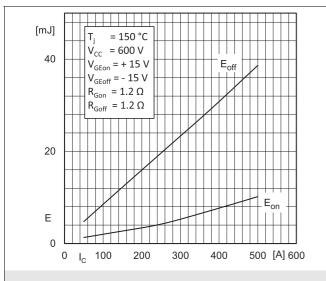


Fig. 3: Typ. turn-on /-off energy = $f(I_C)$

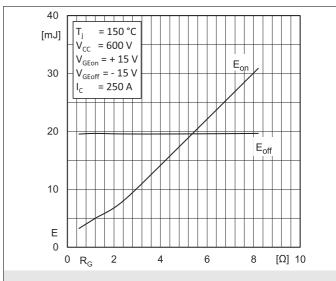


Fig. 4: Typ. turn-on /-off energy = f (R_G)

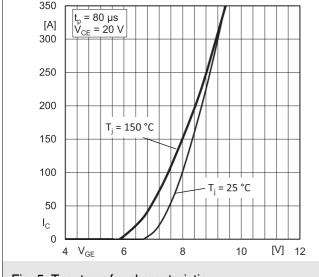


Fig. 5: Typ. transfer characteristic

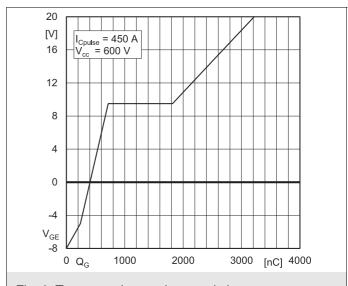
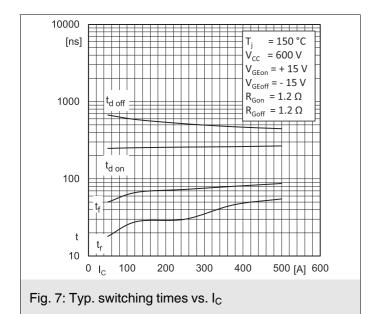
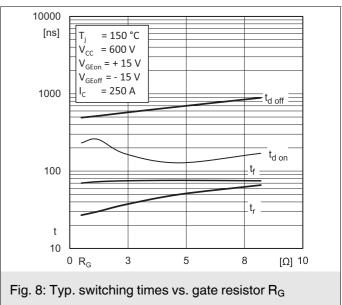
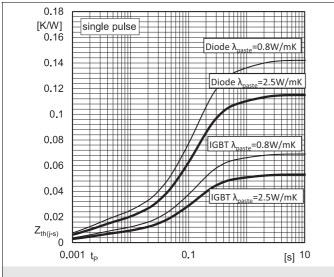
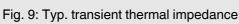


Fig. 6: Typ. gate charge characteristic









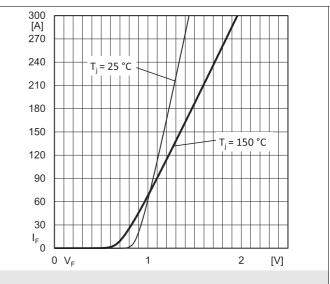


Fig. 10: Typ. CAL diode forward charact., incl. $R_{\text{CC}'+\,\text{EE}'}$

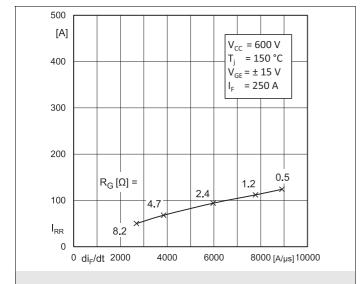
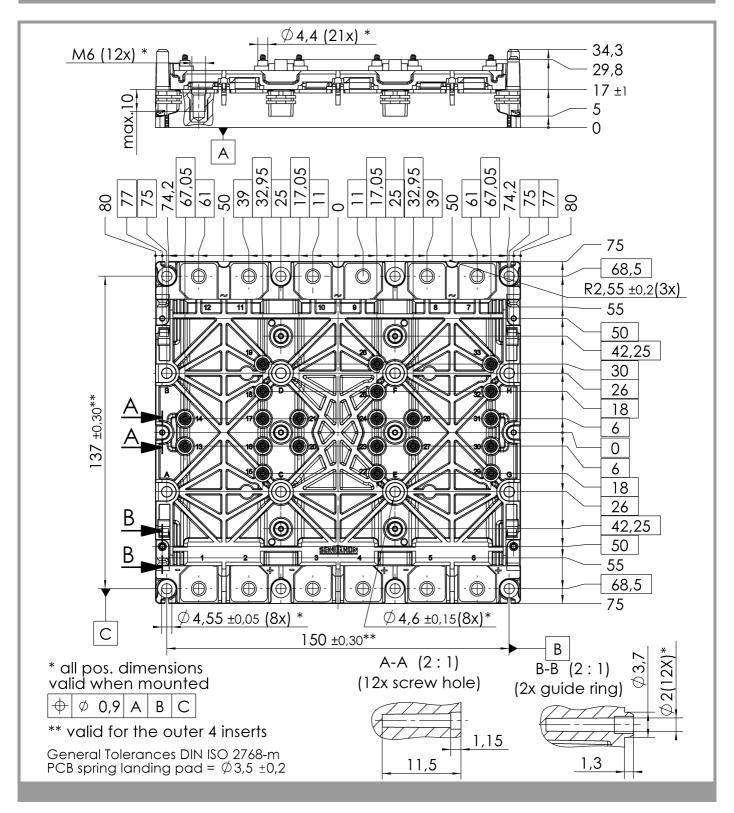
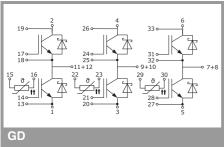


Fig. 11: Typ. CAL diode peak reverse recovery current





This is an electrostatic discharge sensitive device (ESDS) due to international standard IEC 61340.

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