

Plug & Play Driver Board for SEMiX223GB12E4p and SEMiX303GB12E4p

Order Number L5066901 – Driver 27895002 – Module 223GB 27895007 – Module 303GB

SKYPER 12 press-fit C 300A

Features

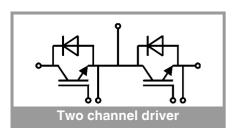
- Dynamic short circuit detection with SoftOff
- Undervoltage lockout on primary side and secondary side
- Internal power supply
- ROHS, UL recognized

Typical Applications*

- Solar inverters
- Power supplies
- Motor drives

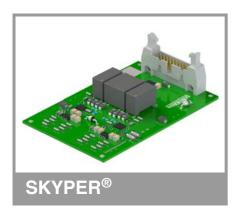
Remarks

- All data refer to
 T_{op} = +25°C and V_S = V_{S(typ)}
 unless otherwise specified
- Environmental conditions please see technical explanation



Absolute Maximum Ratings				
Symbol	Conditions	Values	Unit	
			•	
Vs	Supply voltage primary side	15.7	V	
V _{IH}	Input signal voltage (HIGH)	Vs + 0.4	V	
V_{IL}	Input signal voltage (LOW)	GND - 0.4	V	
f _{max}	Maximum switching frequency ¹⁾	30	kHz	
V _{CE}	Collector emitter voltage ²⁾	1200	V	
V_{DC}	DC-Link voltage ³⁾	800	V	
dv/dt	Rate of rise and fall of voltage secondary to primary side	50	kV/μs	
V _{isol}	Insulation test voltage ⁴⁾	4000	V	
T _{op}	Operating temperature	-40 85	°C	
T _{stg}	Storage temperature	-40 85	°C	

Characteristics					
Symbol	Conditions	min.	typ.	max.	Unit
.,	10			4= 0	1 ,,
Vs	Supply voltage primary side 14.4 15		15	15.6	V
$V_{\text{UVLO(prim)}}$	Undervoltage lockout primary side	12.2 13.9		13.9	V
V _{UVLO(secP)}	Undervoltage lockout secondary side, positive voltage	9.4			V
$V_{\text{UVLO}(\text{secN})}$	Undervoltage lockout secondary side, negative voltage	-5.8		-4.1	V
I _{S(idle)}	Supply current primary side (no load)		120		mA
I _{S(max)}	Supply current primary side (full load)			450	mA
VI	Input signal voltage on/off		Vs/0		V
R _{IN(sw)}	Input resistance (switching signals)		33		kΩ
C _{IN(sw)}	Input capacitance (switching signals)		1		nF
V _{G(on)}	Turn-on output voltage		15		V
$V_{G(off)}$	Turn-off output voltage		-11.3		V
t _{d(on)}	Turn-on propagation delay time	1			μs
t _{d(off)}	Turn-off propagation delay time	1		μs	
R _{IN(err)}			150		kΩ
C _{IN(err)}	Input capacitance (error input) 10			nF	
t _{d(err)}	Error propagation delay time ⁵⁾	0.6			μs
t _{d(err,ext)}	External error propagation delay time ⁶⁾	0.6			μs
t _{IDT}	Interlock dead time ⁷⁾	2			μs
t _{jitter}	Signal transfer time deviation ⁸⁾	± 12.5			ns
t _{SPS}	Short pulse suppression	0.395			μs
t _{POR}	Power-on reset time	0.15		s	
t _{reset}	Error reset time ⁹⁾	0.03		ms	
$V_{\text{CE(ref)}}$	Reference voltage for V _{CE} -monitoring ¹⁰⁾	8.2			V
t _{bl(VCE)}				μs	
R _{OTLO}	Over temperature lockout ¹²⁾ 243			Ω	
R _{G(on)}	Populated gate resistor for turn-on		1.31		Ω
R _{G(off)}	Populated gate resistor for turn-off		0.33		Ω
I _{clear(PS)}	Shortest distance in air, primary side to secondary side	13			mm
I _{clear(SS)}	Shortest distance in air, secondary sides	6.1		mm	
I _{clear(TS)}	Shortest distance in air, temperature sensor pads to secondary side	3.1		mm	
I _{clear(TP)}	Shortest distance in air, temperature sensor pads to primary side	e 13			mm



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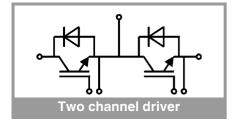
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Characteristics					
Symbol	Conditions min. typ. max		max.	Unit	
I _{creep(PS)}	Shortest distance along the surface, primary side to secondary side	13			mm
I _{creep(SS)}	Shortest distance along the surface, secondary sides	6.1		mm	
I _{creep(TS)}	Shortest distance along the surface, temperature sensor pads to secondary side	6.1			mm
I _{creep(TP)}	Shortest distance along the surface, temperature sensor pads to primary side	13			mm
V _{PDPS}	Partial discharge extinction voltage, primary side to secondary side ¹³⁾	2107			V
V _{PDTP}	Partial discharge extinction voltage, temperature sensor pads to primary side ¹³⁾	2107			V
W	Weight		45		g
MTBF	Mean Time Between Failure ¹⁴⁾	7.5		10 ⁶ h	



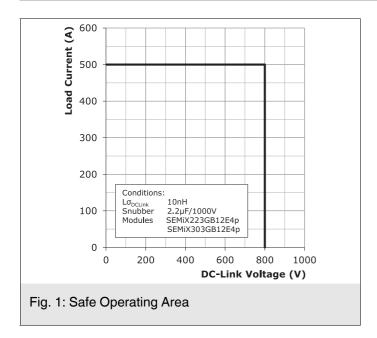
Footnotes

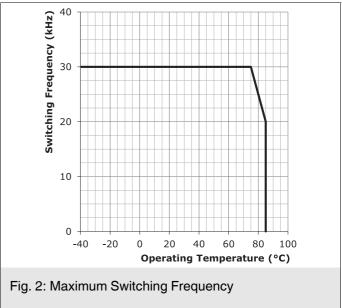
Footnote	Description		
1)	The rated maximum switching frequency is valid for an operating temperature up to 75°C. For operating temperatures above 75°C the limits are specified according to the graph 'Maximum Switching Frequency'.		
2)	Voltage class of semiconductor.		
3)	The DC-Link voltage is limited due to the specified safe operation area boundaries.		
4)	Test Conditions: ACrms, 2s, input to output		
5)	Time between the driver detects an error at the secondary side until the primary side reports an error at the interface.		
6)	Time between the driver receives an external error signal at the primary side until the driver turns off its outputs at the secondary side.		
7)	The interlock dead time prevents the two outputs from being activated simultaneously. The dead time generation starts with each turn-off command at the driver's primary side.		
8)	The jitter is defined as the maximum deviation of the switching signal propagation delay time at constant environmental conditions.		
9)	Minimum time of the driver in error state.		
10)	The driver detects a desaturation event, when one of its outputs is in on-state and the applied voltage at the corresponding V _{CE} -monitoring input exceeds the reference voltage for V _{CE} -monitoring.		
11)	Time elapsed when the driver starts the power semiconductor's turn-on process after the V_{CE} -monitoring input has fallen below the reference voltage for V_{CE} -monitoring.		
12)	The driver sets both outputs to $V_{G(off)}$, if the ohmic resistance of the applied temperature sensor is less than R_{OTLO} . For proper function of the temperature lockout a NTC temperature sensor has to be used. The temperature sensor of the SEMIKRON's SEMiX 3 press-fit modules has an ohmic resistance of R_{OTLO} at 130°C, typically.		
13)	The partial discharge extinction voltage in this data sheet is defined as peak voltage.		
14)	Conditions: T _{op} = 40°C; full load		

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Signal Connector

PIN	Signal	Function	Specifications	
X10:01	reserved		Open pin	
X10:02	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:03	reserved		Open pin	
X10:04	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:05	PWR_VS	Driver power supply	Stabilised +15V ±4%	
X10:06	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:07	nERR_IN	Error input	15V logic inverted; 150kΩ/10nF (pull-up) LOW = External error HIGH = No external error	
X10:08	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:09	nERR_OUT	Error output	Open collector output; max. 18V/15mA (external pull-up resistor needed) LOW = Error HIGH = No error	
X10:10	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:11	TOP_IN	Switching signal input (TOP)	15V logic; $33k\Omega/1nF$ (pull-down) LOW = TOP switch off HIGH = TOP switch on	
X10:12	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:13	nERR_OUT	Error output	Open collector output; max. 18V/15mA (external pull-up resistor needed) LOW = Error HIGH = No error	
X10:14	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:15	BOT_IN	Switching signal input (BOT)	15V logic; $33k\Omega/1nF$ (pull-down) LOW = BOT switch off HIGH = BOT switch on	
X10:16	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:17	reserved		Open pin	
X10:18	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	
X10:19	reserved		Open pin	
X10:20	PWR_GND	Ground potential for power supply and digital inputs	To be connected to ground	





This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, chapter IX.

*IMPORTANT INFORMATION AND WARNINGS

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