

Insulated Gate Bi-Polar Transistor

Type T1800GB45A

Absolute Maximum Ratings

	VOLTAGE RATINGS	MAXIMUM LIMITS	UNITS
V_{CES}	Collector – emitter voltage	4500	V
$V_{DC\ link}$	Permanent DC voltage for 100 FIT failure rate.	2800	V
V_{GES}	Peak gate – emitter voltage	± 20	V

	RATINGS	MAXIMUM LIMITS	UNITS
I_C	DC collector current, IGBT	1800	A
I_{CRM}	Repetitive peak collector current, $t_p=1ms$, IGBT	3600	A
$I_{F(DC)}$	Continuous DC forward current, Diode	1800	A
I_{FRM}	Repetitive peak forward current, $t_p=1ms$, Diode	3600	A
I_{FSM}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}=60\%V_{RRM}$, Diode (Note 4)	25.7	A
I_{FSM2}	Peak non-repetitive surge $t_p=10ms$, $V_{RM}\leq 10V$, Diode (Note 4)	28.3	A
P_{MAX}	Maximum power dissipation, IGBT (Note 2)	13.7	kW
$(di/dt)_{cr}$	Critical diode di/dt (note 3)	3500	A/ μs
T_j	Operating temperature range.	-40 to +125	$^{\circ}C$
T_{stg}	Storage temperature range.	-40 to +125	$^{\circ}C$

Notes: -

- 1) Unless otherwise indicated $T_j = 125^{\circ}C$.
- 2) $T_{sink} = 25^{\circ}C$, double side cooled.
- 3) Maximum commutation loop inductance 200nH.
- 4) Half-sinewave, $125^{\circ}C$ T_j initial.

Characteristics

IGBT Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _{CE(sat)}	Collector – emitter saturation voltage	-	2.8	3.2	I _C = 1800A, V _{GE} = 15V, T _j = 25°C	V
		-	3.60	4.0	I _C = 1800A, V _{GE} = 15V	V
V _{T0}	Threshold voltage	-	-	1.82	Current range: 600A – 1800A	V
r _T	Slope resistance	-	-	1.21		mΩ
V _{GE(TH)}	Gate threshold voltage	-	5.1	-	V _{CE} = V _{GE} , I _C = 180mA	V
I _{CES}	Collector – emitter cut-off current	-	45	70	V _{CE} = V _{CES} , V _{GE} = 0V	mA
I _{GES}	Gate leakage current	-	-	±20	V _{GE} = ±20V	µA
C _{ies}	Input capacitance	-	280	-	V _{CE} = 25V, V _{GE} = 0V, f = 1MHz	nF
t _{d(on)}	Turn-on delay time	-	1.5	-	I _C = 1800A, V _{CE} = 2800V, di/dt = 3000A/µs	µs
t _{r(V)}	Rise time	-	3.3	-		µs
Q _{g(on)}	Turn-on gate charge	-	12.5	-	V _{GE} = ±15V, L _s = 200nH	µC
E _{on}	Turn-on energy	-	11	-	R _{g(ON)} = 3Ω, R _{g(OFF)} = 11Ω, C _{GE} = 183nF	J
t _{d(off)}	Turn-off delay time	-	4.7	-	Integral diode used as freewheel diode (Note 3 & 4)	µs
t _{r(I)}	Fall time	-	2.5	-		µs
Q _{g(off)}	Turn-off gate charge	-	10	-		µC
E _{off}	Turn-off energy	-	10.5	-		J
I _{SC}	Short circuit current	-	5500	-	V _{GE} = +15V, V _{CC} = 2800V, V _{CEmax} ≤ V _{CES} , t _p ≤ 10µs	A

Diode Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
V _F	Forward voltage	-	3.7	4.0	I _F = 1800A, T _j = 25°C	V
		-	3.9	4.2	I _F = 1800A	V
V _{To}	Threshold voltage	-	-	2.27	Current range 600A - 1800A	V
r _T	Slope resistance	-	-	1.07		mΩ
I _{rm}	Peak reverse recovery current	-	1600	-	I _F = 1800A, V _r = 2800V, V _{GE} = -15V, di/dt = 3000A/µs	A
Q _{rr}	Recovered charge	-	2000	-		µC
t _{rr}	Reverse recovery time, 50% chord	-	1.6	-		µs
E _r	Reverse recovery energy	-	2.8	-		J

Thermal Characteristics

	PARAMETER	MIN	TYP	MAX	TEST CONDITIONS	UNITS
R _{thJK}	Thermal resistance junction to sink, IGBT	-	-	7.3	Double side cooled	K/kW
		-	-	11.9	Collector side cooled	K/kW
		-	-	19	Emitter side cooled	K/kW
R _{thJK}	Thermal resistance junction to sink, Diode	-	-	14.4	Double side cooled	K/kW
		-	-	22.3	Cathode side cooled	K/kW
		-	-	41.1	Anode side cooled	K/kW
F	Mounting force	50	-	70	Note 2	kN
W _t	Weight	-	2	-		kg

Notes:-

- 1) Unless otherwise indicated T_j = 125°C.
- 2) Consult application note 2008AN01 for detailed mounting requirements
- 3) C_{GE} is additional gate – emitter capacitance added to output of gate drive
- 4) Figures 6 to 9 are obtained using integral diode as freewheeling diode

Curves

Figure 1 – Typical collector-emitter saturation voltage characteristics

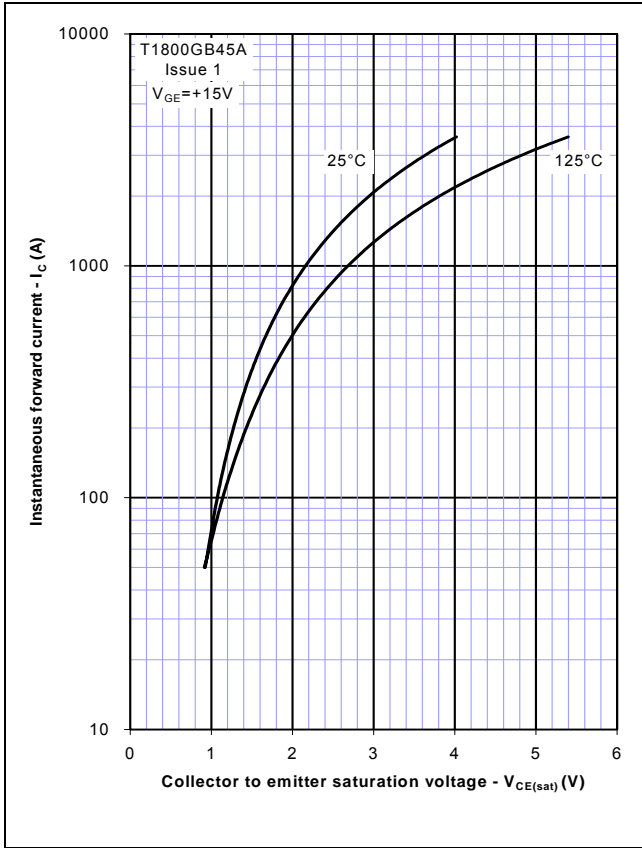


Figure 2 – Typical output characteristic

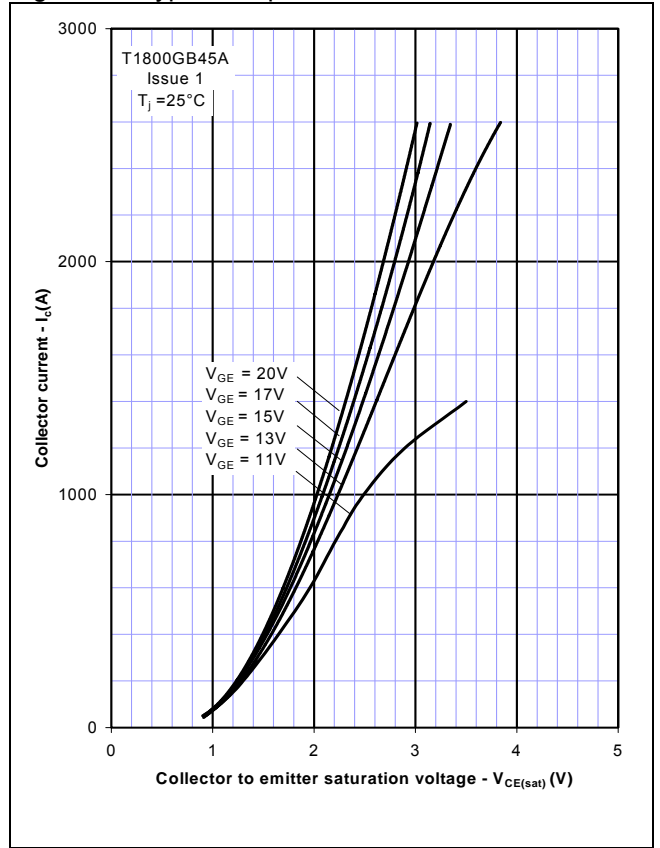


Figure 3 – Typical output characteristic

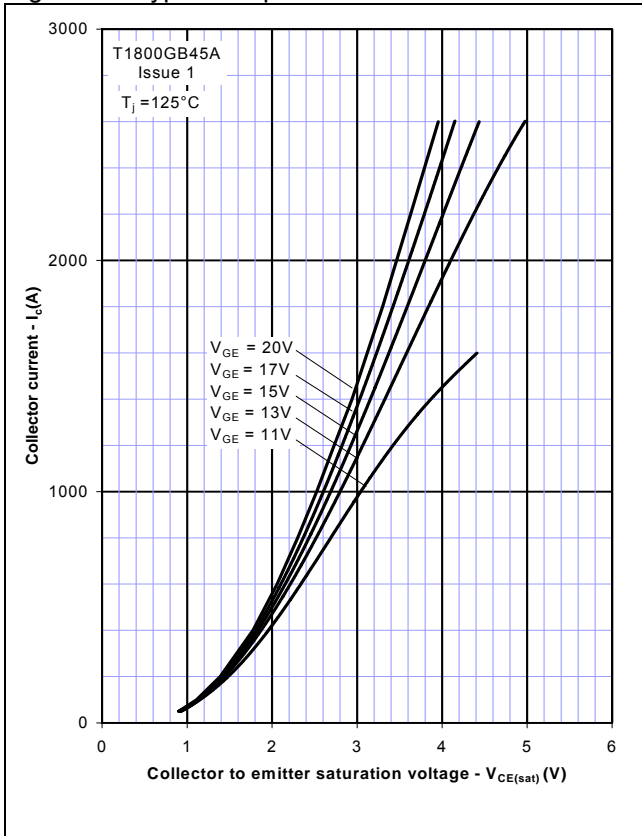


Figure 4 – Typical turn-on delay time vs gate resistance

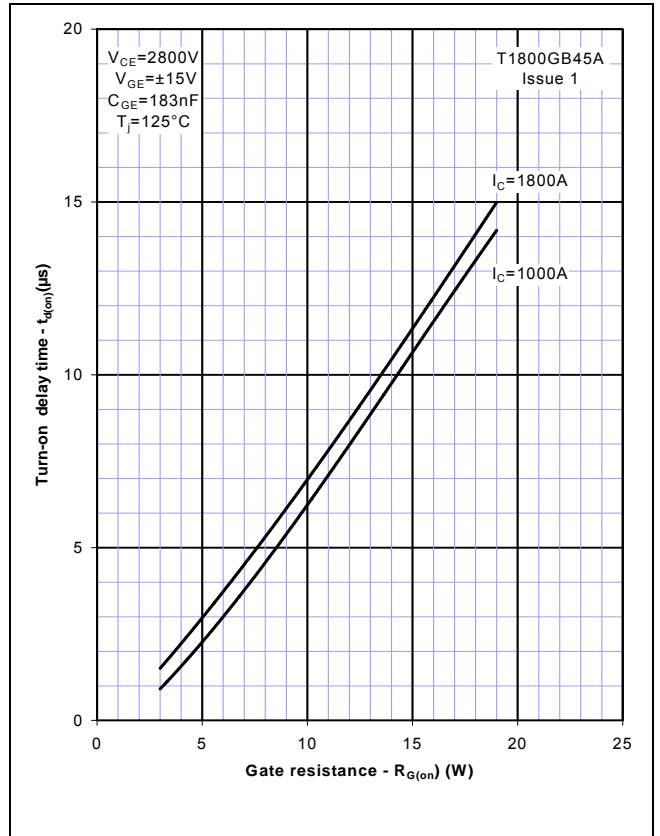


Figure 5 – Typical turn-off delay time vs. gate resistance

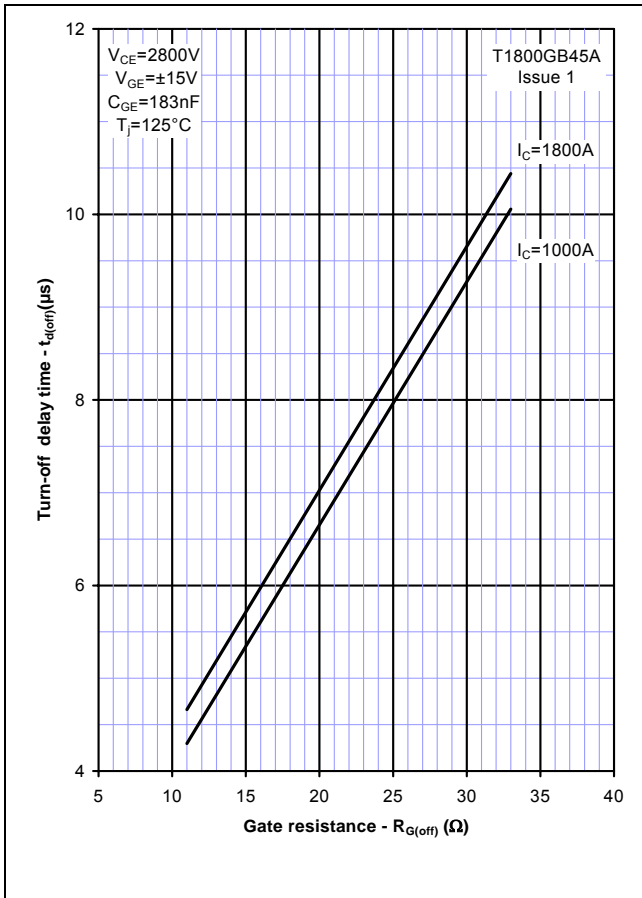


Figure 6 – Typical turn-on energy vs. collector current

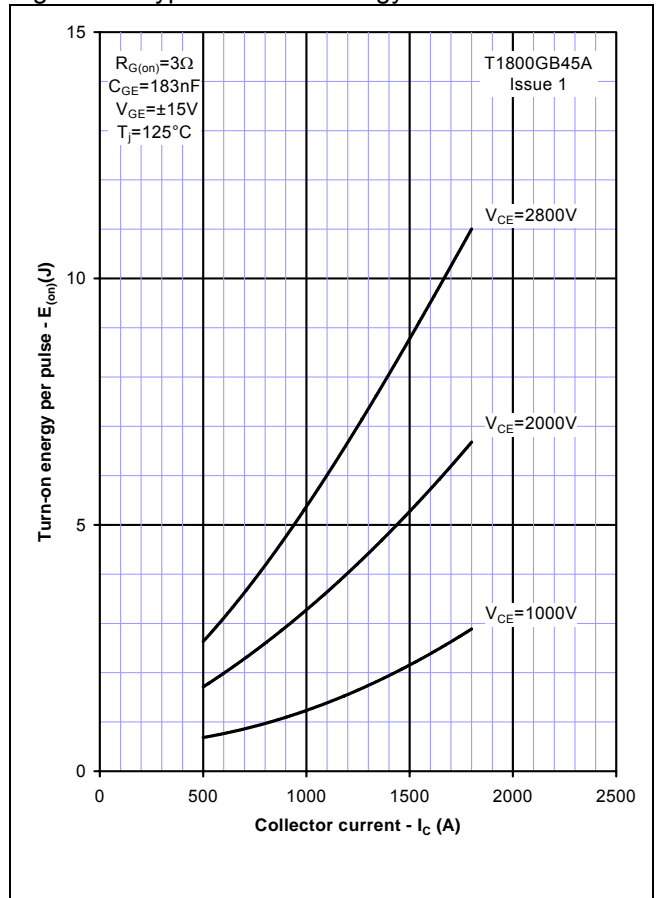


Figure 7 – Typical turn-on energy vs. di/dt

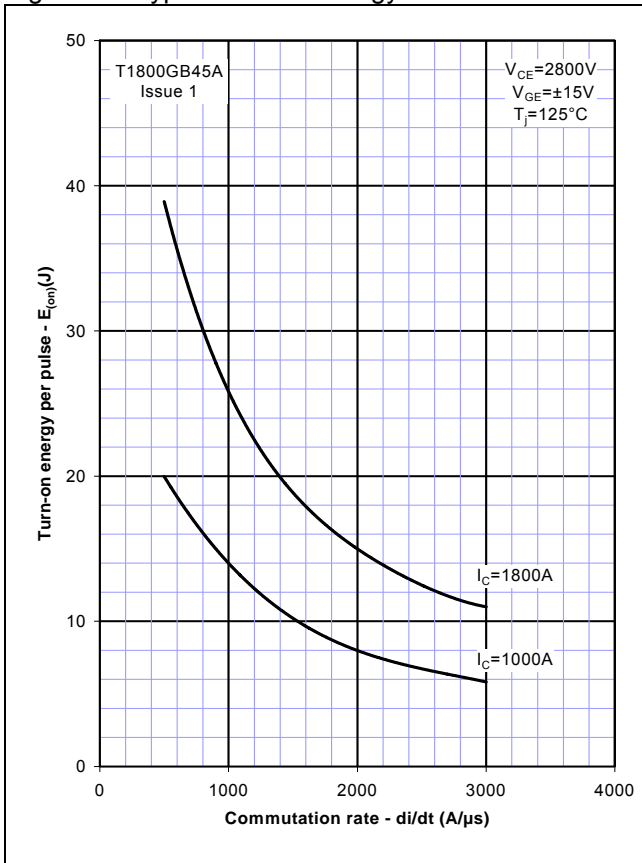


Figure 8 – Typical turn-off energy vs. collector current

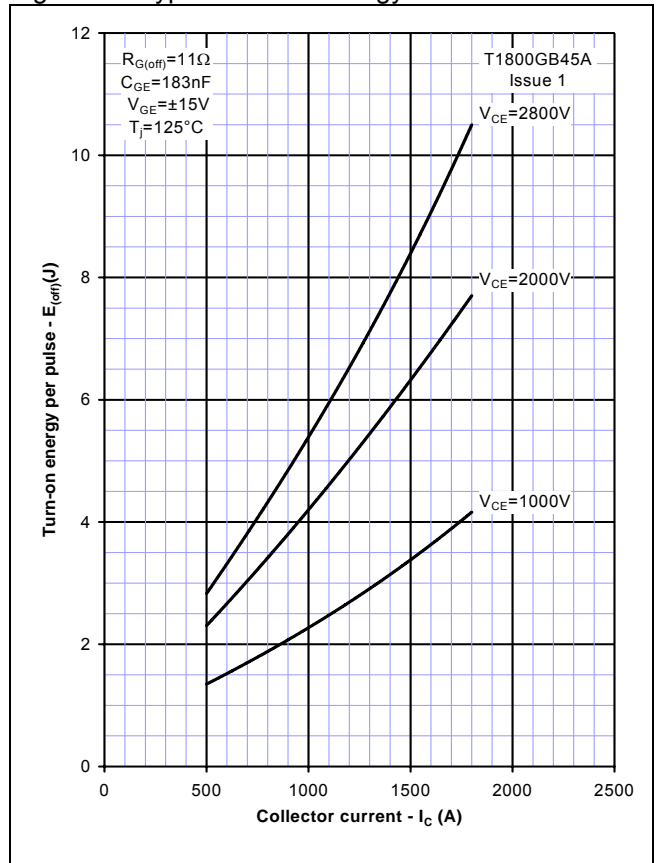


Figure 9 – Turn-off energy vs voltage

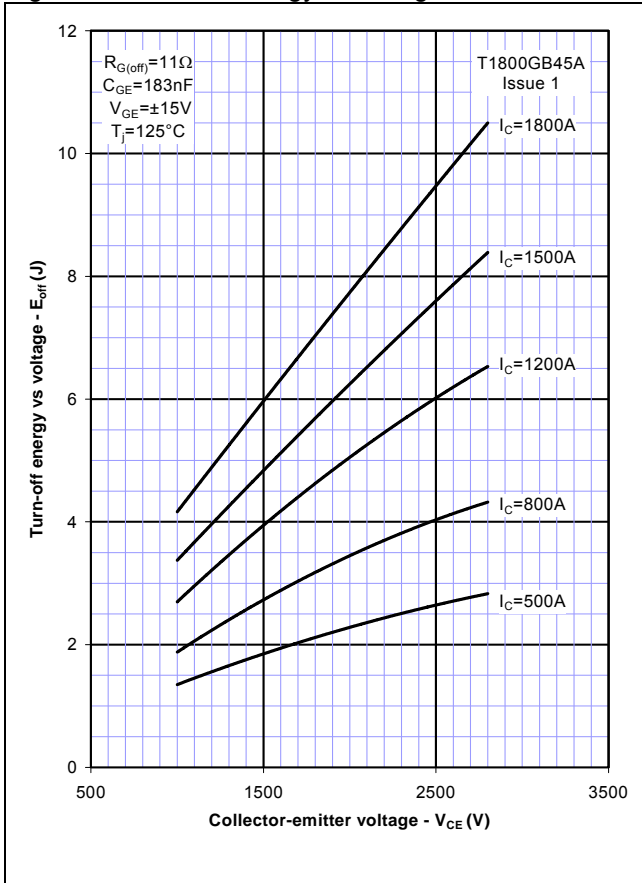


Figure 10 – Safe operating area (IGBT)

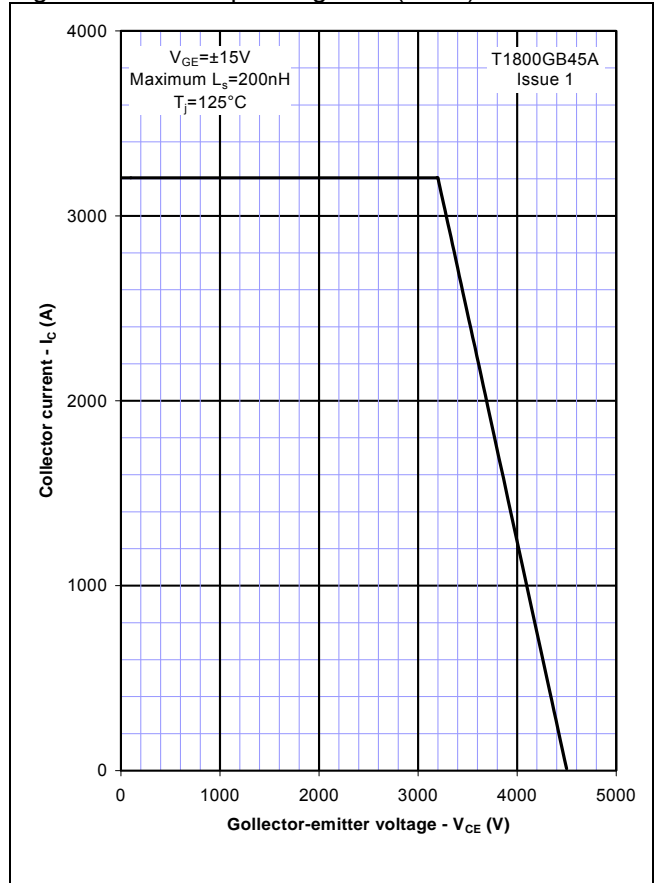


Figure 11 – Typical diode forward characteristics

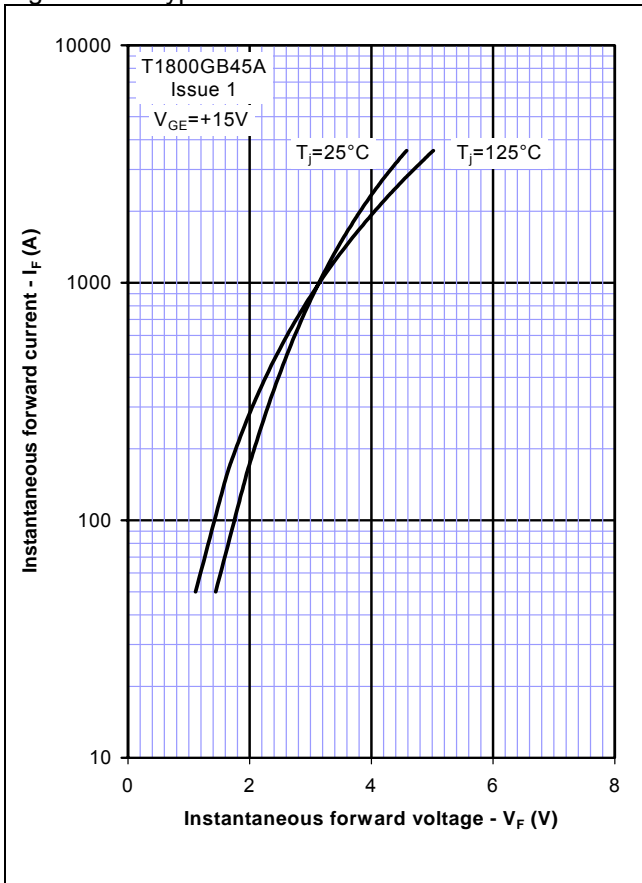


Figure 12 – Typical recovered charge

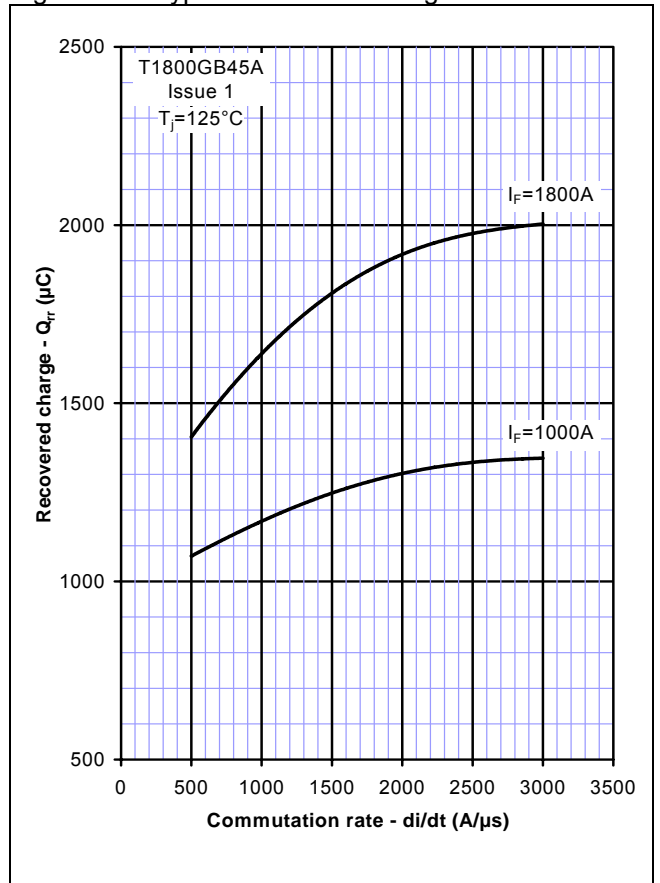


Figure 13 – Typical reverse recovery current

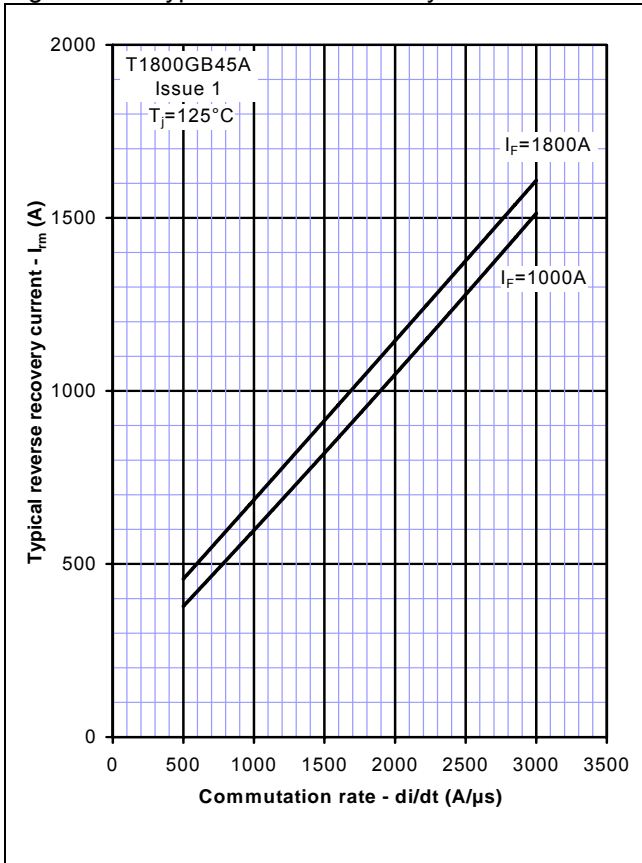


Figure 14 – Typical reverse recovery time

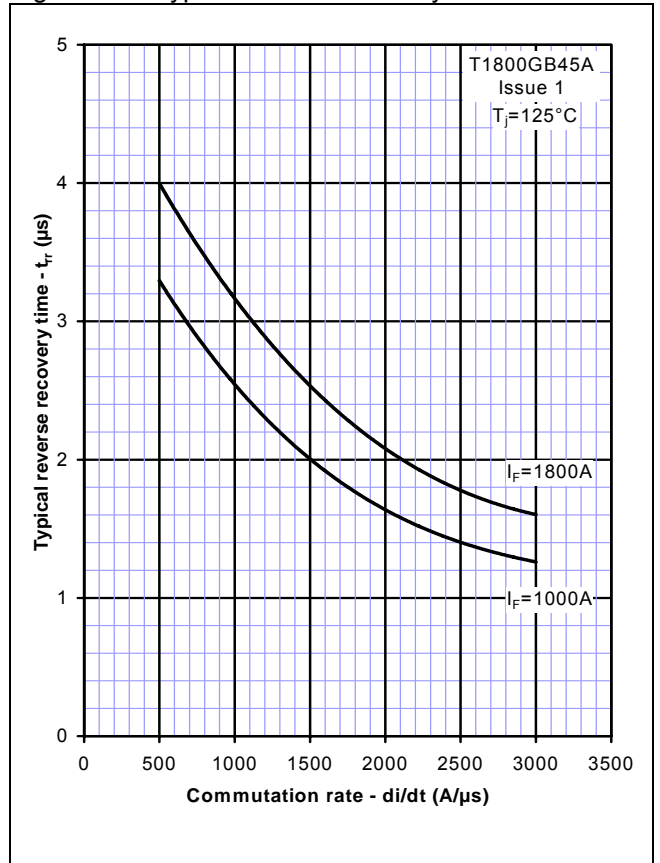


Figure 15 – Typical reverse recovery energy

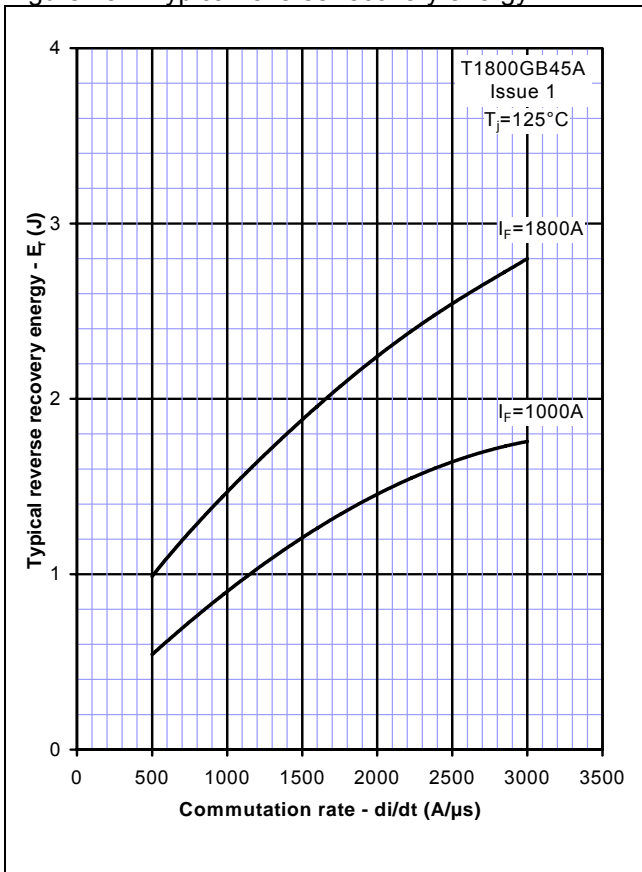


Figure 16 – Safe operating area (Diode)

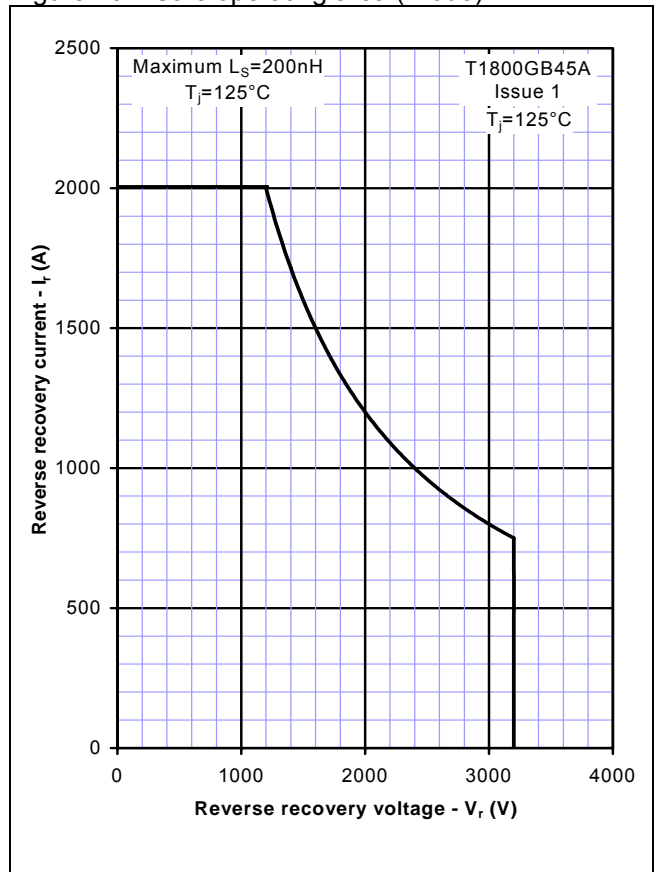


Figure 17 – Transient thermal impedance (IGBT)

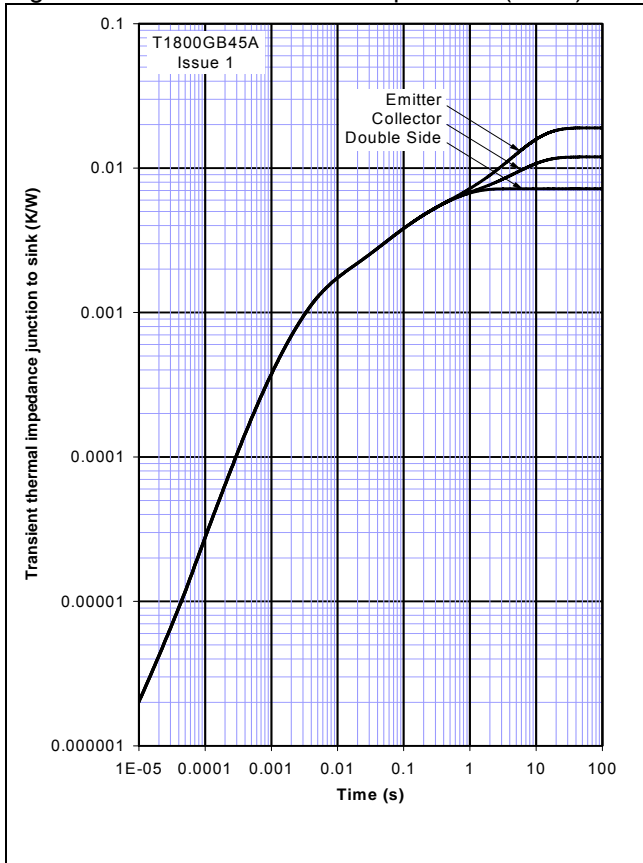
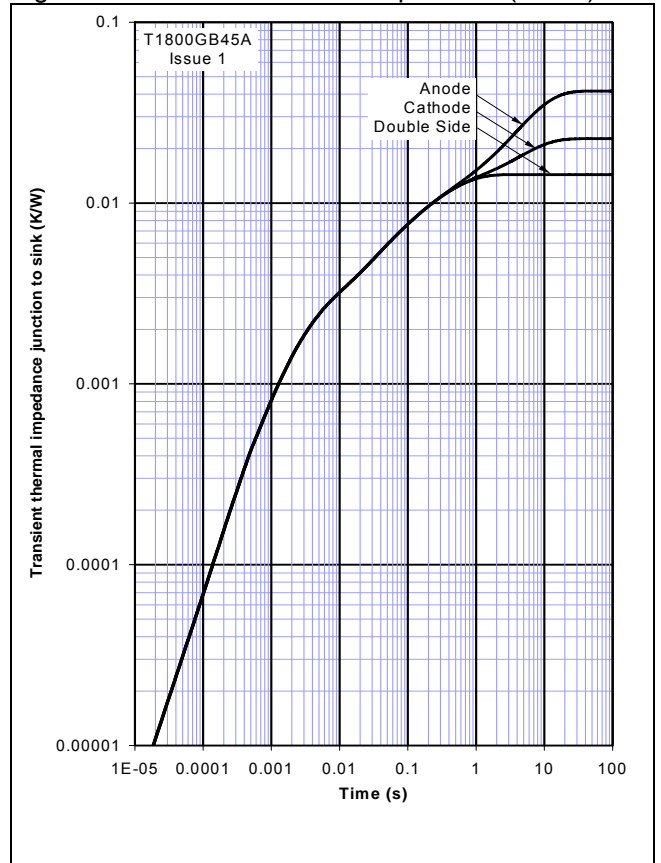
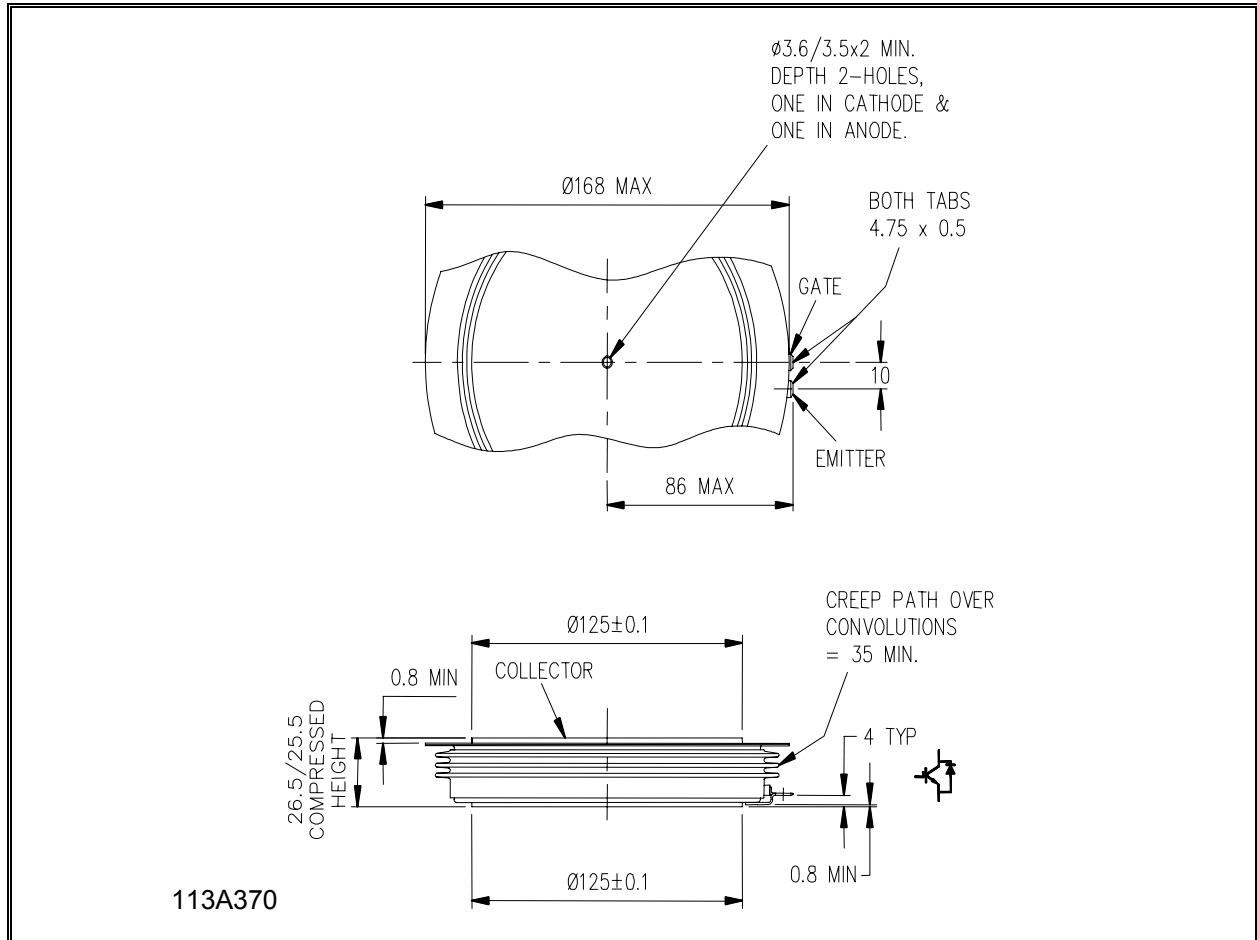


Figure 18 – Transient thermal impedance (Diode)



Outline Drawing & Ordering Information



ORDERING INFORMATION			
(Please quote 10 digit code as below)			
T1800	GB	45	A
Fixed type Code	Fixed Outline Code	Voltage Grade $V_{CES}/100$ 45	Fixed format code
Typical order code: T1800GB45A ($V_{CES} = 4500V$)			

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